

PART II.

MEMORY CONFIGURATION

Before using the memory, you must properly configure the PC system board. The system board configuration please refer to the PC Manual.

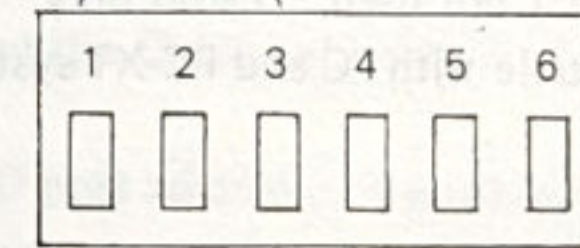
2.1 Memory Switch Settings

Switch S1 and Jumper JP3 controls three different functions related to the memory on the card. The three functions which must be configured are:

1. The starting address
2. The amount of memory installed,
3. Parity checking (enable or disabled)

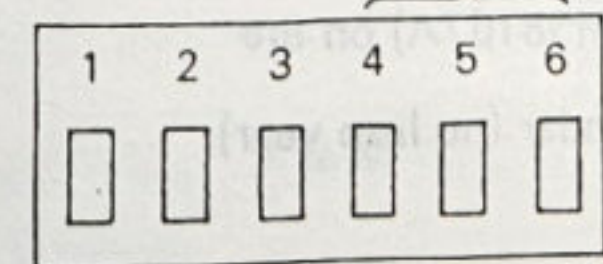
2.1-1 Set the Starting Memory Address

Starting Address	S1	S2	S3	Maximum RAM
0K (:00000)	OFF	OFF	OFF	384K
64K (:10000)	OFF	OFF	ON	384K
128K (:20000)	OFF	ON	OFF	384K
192K (:30000)	OFF	ON	ON	384K
256K (:40000)	ON	OFF	OFF	384K
320K (:50000)	ON	OFF	ON	320K
384K (:60000)	ON	ON	OFF	256K
448K (:70000)	ON	ON	ON	192K



2.1-2 Amount of Memory Installed

Bank Installed	S4	S5	S6	Total
NONE	OFF	OFF	OFF	0K
1	OFF	OFF	ON	64K
2 (1-2)	OFF	ON	OFF	128K
3 (1-3)	OFF	ON	ON	192K
4 (1-4)	ON	OFF	OFF	256K
5 (1-5)	ON	OFF	ON	320K
6 (1-6)	ON	ON	OFF	384K



2.1-3 Parity Check Enable

Jumper block JP3 is used for enable/disable the memory parity check, when JP3 is jumpered with a shorting plug, the parity check is enabled. Without a shorting plug on JP3, the memory parity check is disabled.

2.2 Installing Additional Memory

A configured with less than its 384K maximum memory can be upgraded at any time by installing additional 64K RAM sets. The correct type of chip to be used is 64K dynamic memory, 200 nanosecond access time, pin 1 not used, +5 Volt only. The following memory chips are compatible with PC and PC-XT system board:

3. F

Fujitsu MB8264-20

Micron Technology

MT4264-3 or MT4264-20

Mitsubishi M5K4164NS-20

Hitachi HM4864P-3

Motorola MCM6665AL-20

or MCM6665AP-20

T.I. TMS4164-20NLJ

III. THE CLOCK-CALENDAR

The Clock Calendar has following features:

1. 24-hour clock, maintained in a Microprocessor Real Time Clock chip (MM58167A) on the
2. Four-year calendar (no leap year)